U.S. DE RTMENT OF COMMERCE TRADUST AND TRADEMARK OFFICE ATTY DOCKET NO. SERIAL NO. Form PTO 1449 (Modified) 10/623,559 240522US2S **APPLICANT** LIST OF REFERENCES CITED BY APPLICANT Yoshihisa IWATA **FILING DATE GROUP** July 22, 2003 **U.S. PATENT DOCUMENTS DOCUMENT EXAMINER** SUB **FILING DATE** DATE CLASS NAME INITIAL NUMBER CLASS IF APPROPRIATE Mov 5,349,302 09/20/94 L. K. COOPER AA AB AC AD ΑE AF AG AH AI AJ AK AL AM AN **FOREIGN PATENT DOCUMENTS TRANSLATION** DOCUMENT COUNTRY DATE NUMBER YES NO AO AP AQ AR AS AT AU ΑV OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) R. SCHEUERLEIN, et al., IEEE ISSCC Digest of Technical Papers, pages 128-129 (and 94-95 and 409-410), "A 10NS READ AND WRITE NON-VOLATILE MEMORY ARRAY USING A MAGNETIC TUNNEL JUNCTION AND FET AW SWITCH IN EACH CELL*, February 2000 R. CRISP, et al., IEEE ISSCC Digest of Technical Papers, pages 226-227 (and 176-177, 414-415 and 461), "DEVELOPMENT OF SINGLE-CHIP MULTI-GB/s DRAMs", February 7, 1997 AY ΑZ Additional References sheet(s) attached **Date Considered** Examiner *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in

conformance and not considered. Include copy of this form with next communication to applicant.

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